

Fig. 1

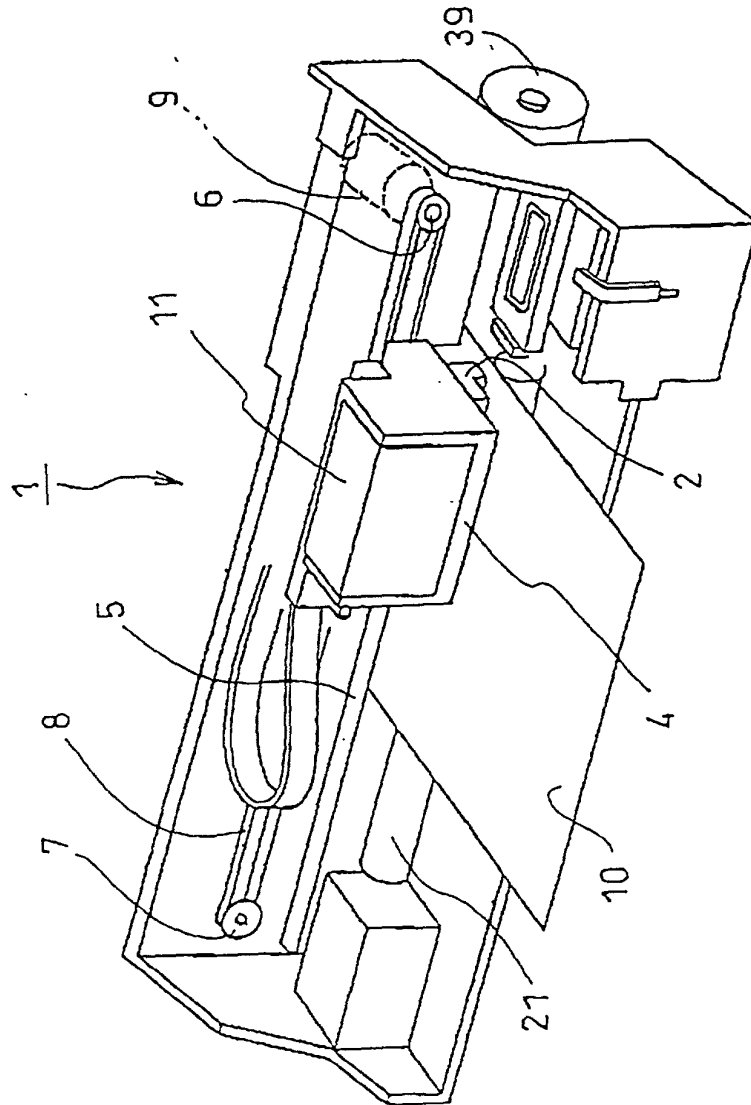


Fig. 2

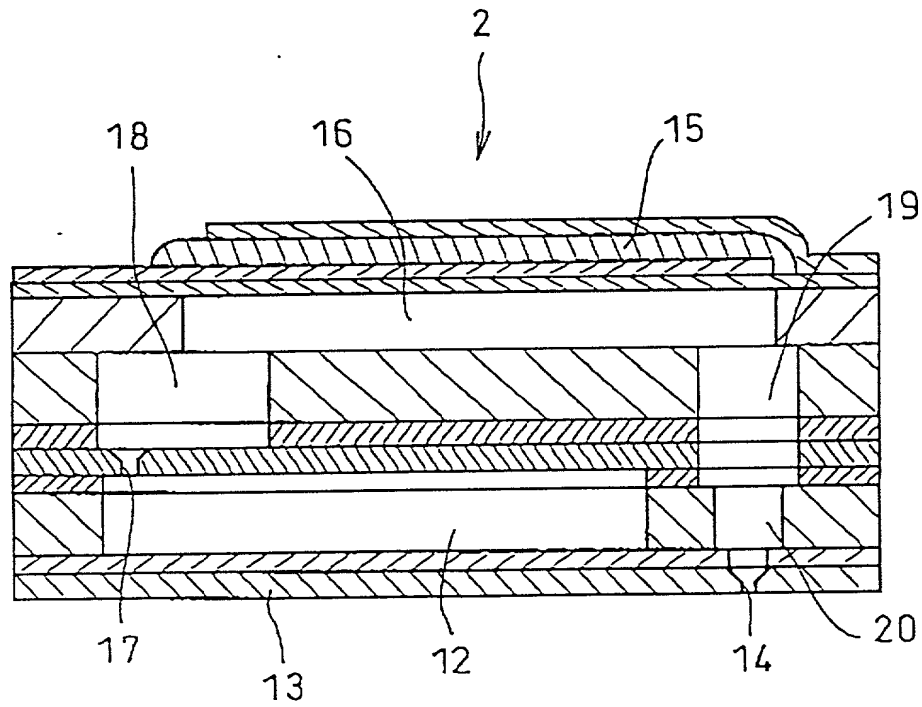
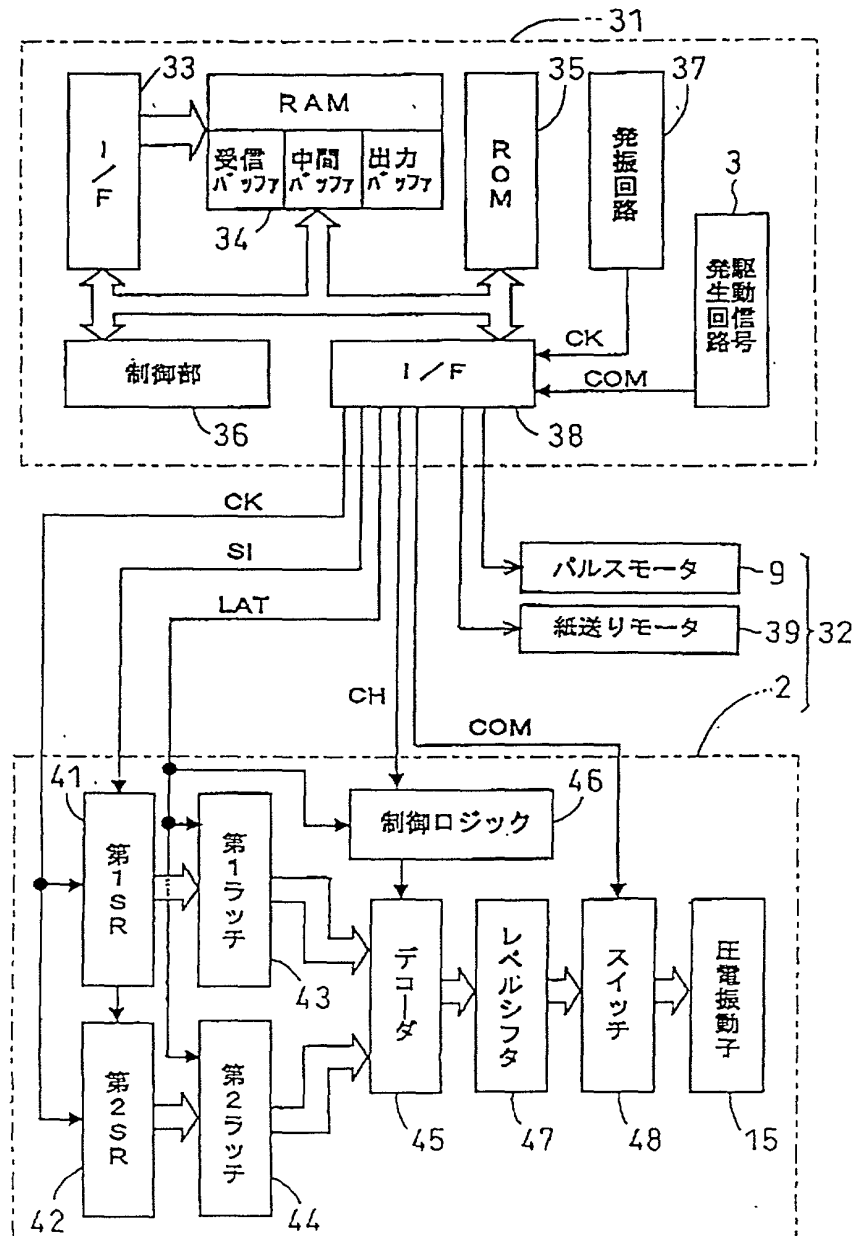


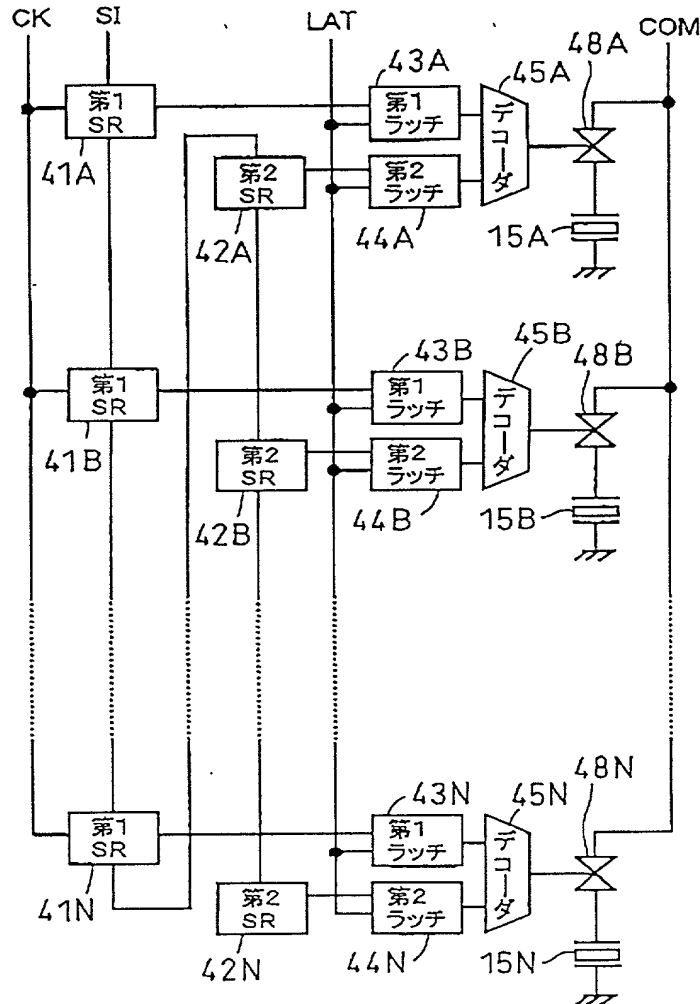
FIG. 2

Fig. 3



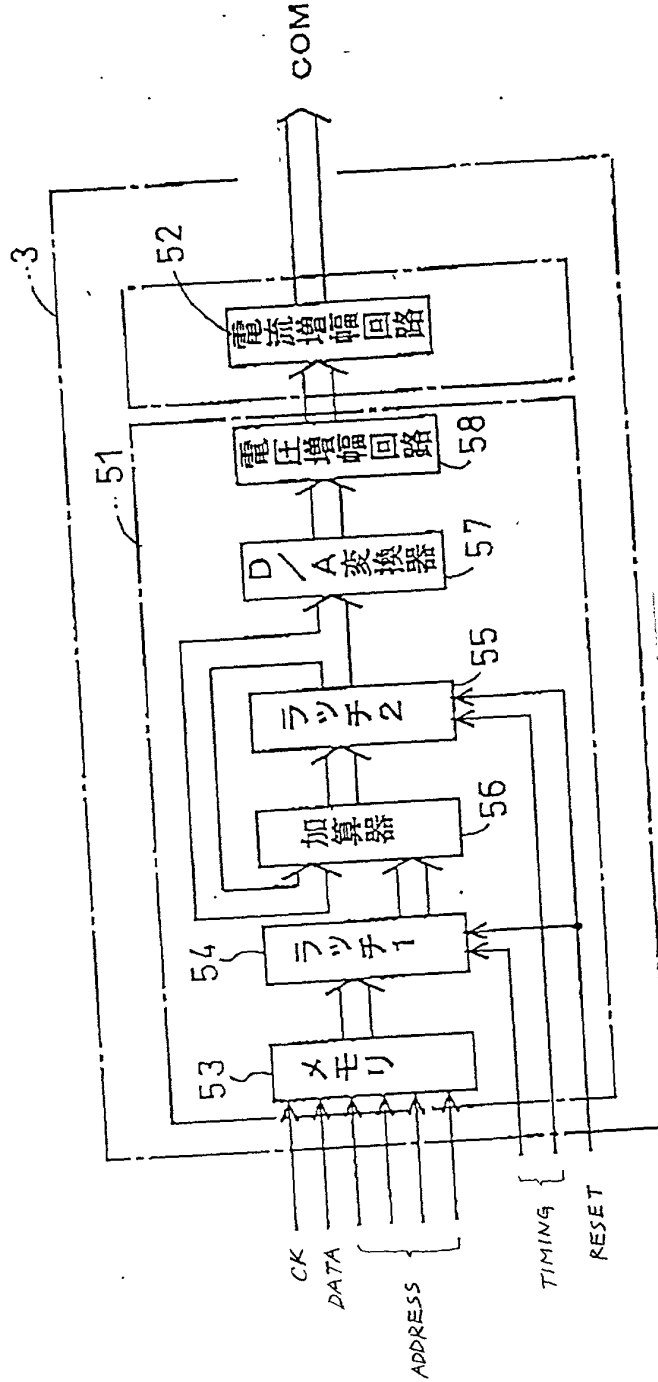
- | | |
|--|-----------------------------------|
| 3: drive signal generator | 41: first shift register section |
| 9: pulse motor | 42: second shift register section |
| 15: piezoelectric vibrator | 43: first latching section |
| 33: external I/F | 44: second latching section |
| 34: reception buffer, intermediate buffer, output buffer | 45: decoder |
| 36: controller | 46: control logic |
| 37: oscillator | 47: level shifter |
| 38: internal I/F | 48: switcher |
| 39: paper feeding motor | |

Fig. 4



- 41A ... 41N: first shift register element
- 42A ... 42N: second shift register element
- 43A ... 43N: first latching element
- 44A ... 44N: second latching element
- 45A ... 45N: decoder element

Fig. 5



- 52: current amplifier
- 53: waveform memory
- 54: first waveform latching section
- 55: second waveform latching section
- 56: adder
- 57: D/A converter
- 58: voltage amplifier

Fig. 6

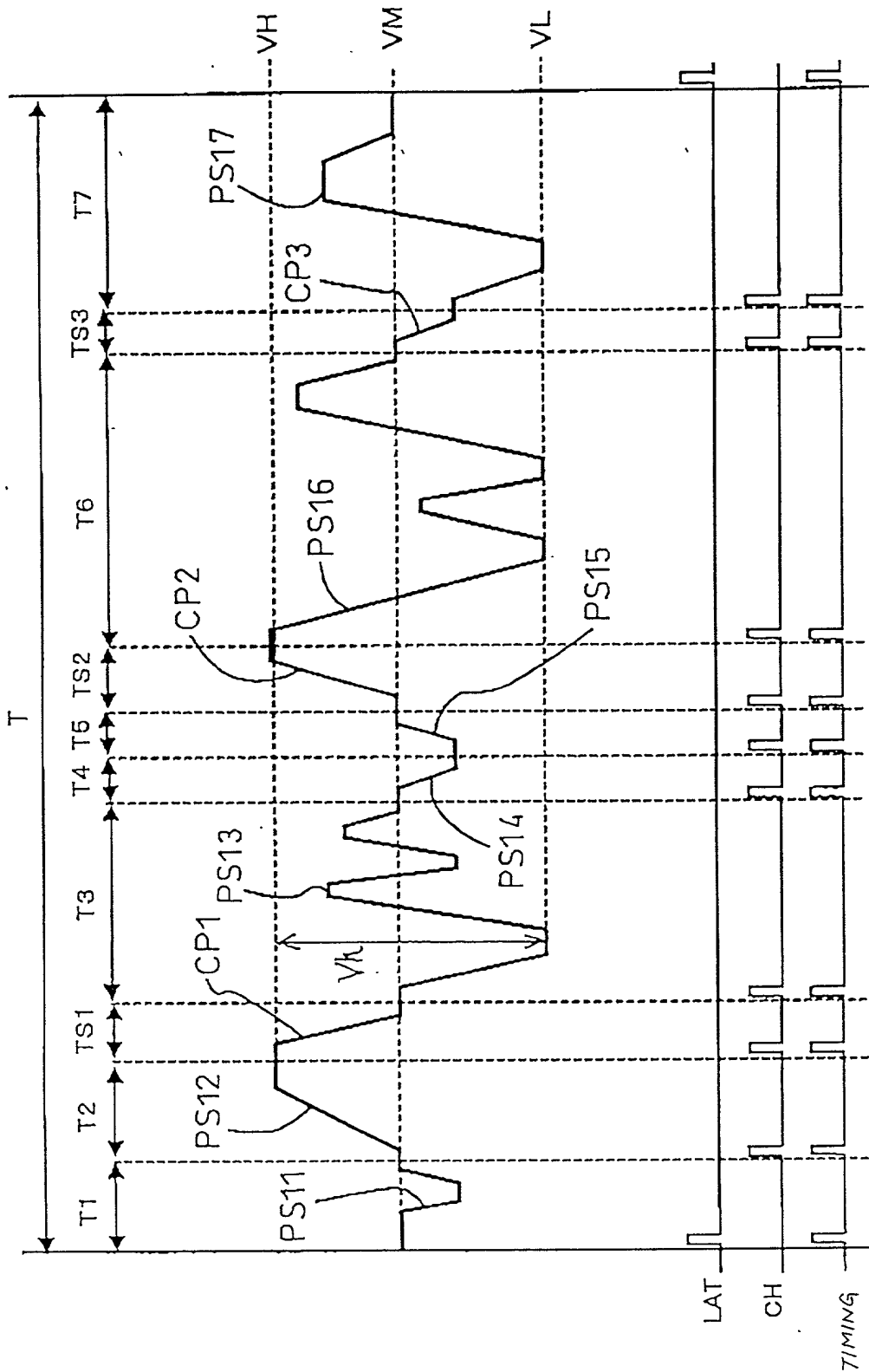
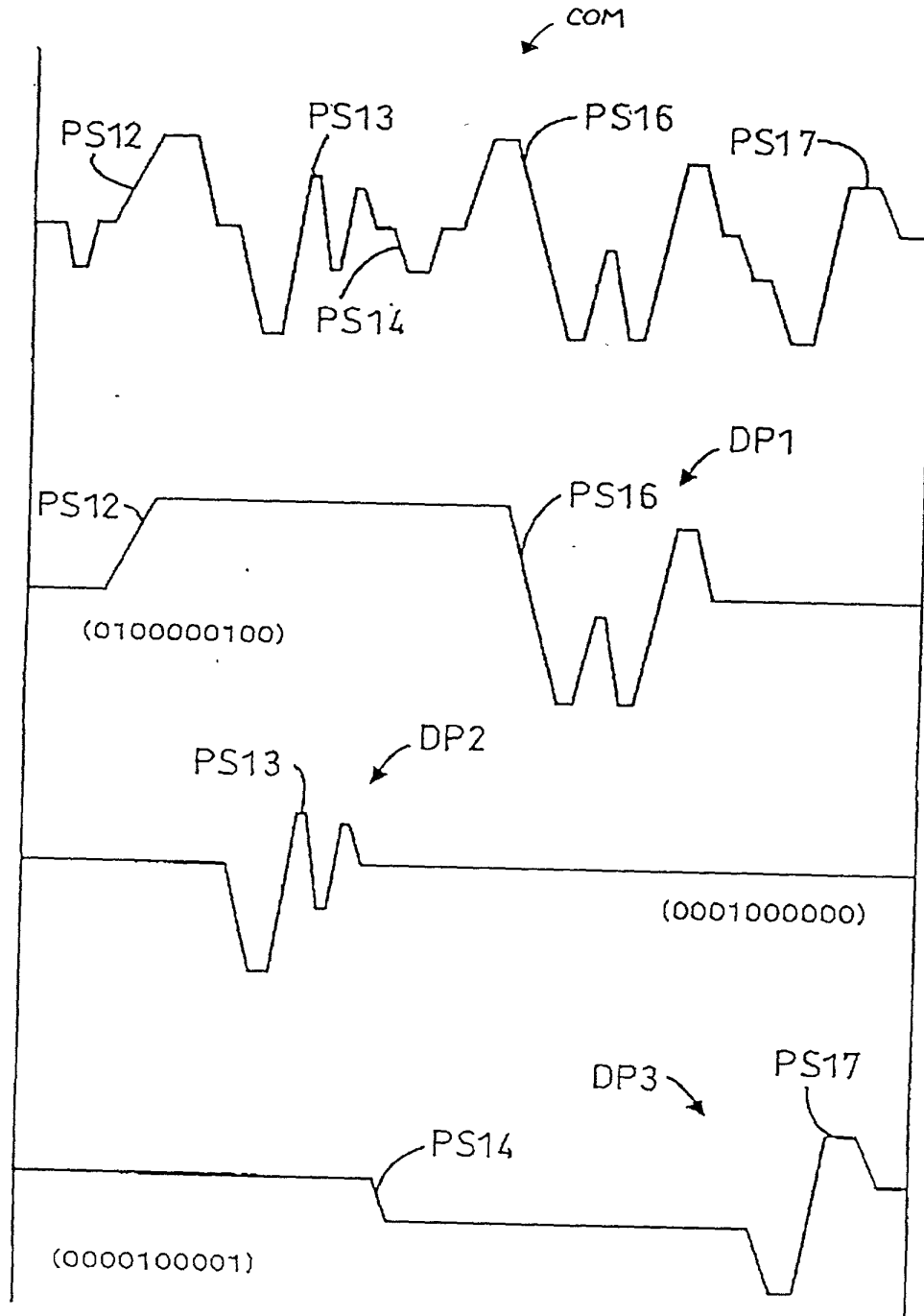


Fig. 7



0992463-080601

T09080" E89T2660

Fig. 8

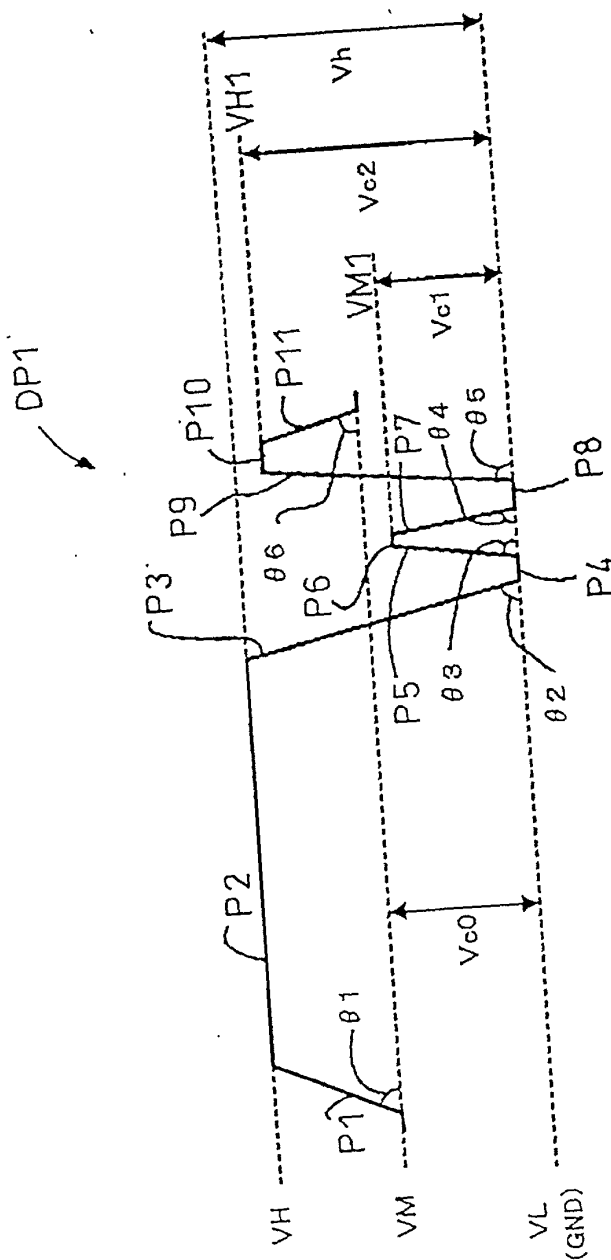


Fig. 9A

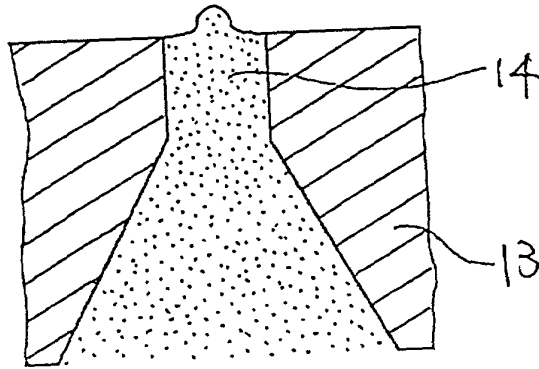


Fig. 9B

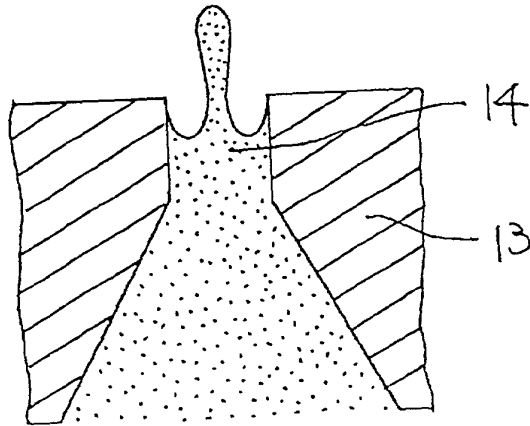


Fig. 9C

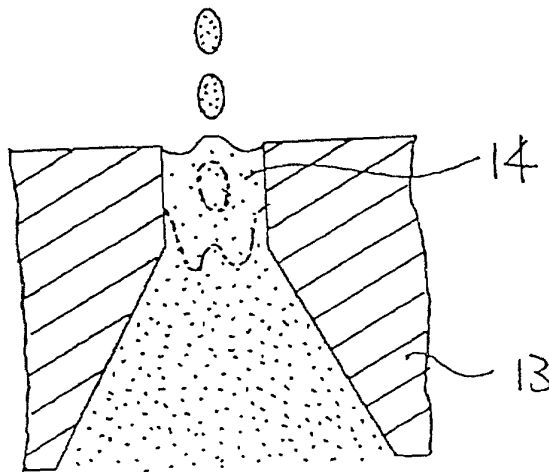


Fig. 10

